



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Rajesh Sundaram, et al. § Group Art Unit: 2827  
§  
Serial No.: 10/713,841 §  
§ Examiner: Thong Quoc Le  
Filed: November 13, 2003 §  
§  
For: Negatively Biasing § Atty. Dkt. No.: ITL.1062US  
Deselected Memory Cells § (P17921)  
§ Assignee: Intel Corporation

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT SUBMITTED WITH RCE**

Sir:

This paper is submitted in conjunction with the enclosed Request for Continuing Examination (RCE) in the above-identified Patent Application. Further examination and reconsideration are respectfully requested in view of the Amendments and Remarks set forth below.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.

Date of Deposit: December 14, 2005  
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Jennifer Juarez